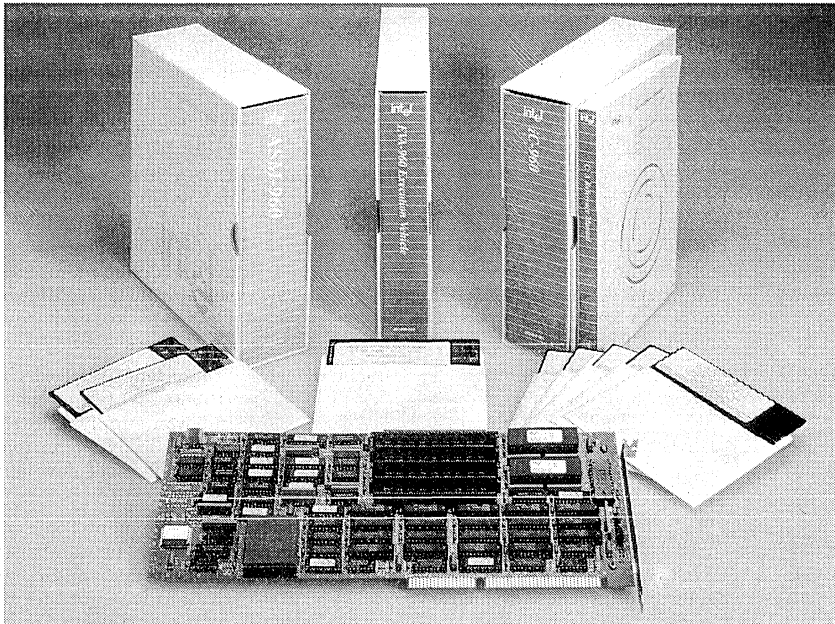


## EXV-960MC EXECUTION VEHICLE



280879-1

### ***80960MC-BASED TARGET SYSTEM SUPPORTING EARLY SOFTWARE DEVELOPMENT AND BENCHMARKING***

EXV-960MC is a software execution vehicle designed to support 80960MC-based designs. Users can use the EXV-960MC board to execute and debug their application software before a functional hardware prototype is available. The EXV-960MC is also designed with programmable waitstate SRAM to support benchmarking activities. The EXV-960MC is supported by the complete set of Intel C, assembler and Ada code generation tools. Both of the VAX/VMS\*-hosted 80960MC software debuggers, the SDM-960MC system debug monitor and the Ada-960MC source-level debugger, can be used for debugging software running on the EXV-960MC.

EXV-960MC includes a Multibus I form factor board and a set of SDM-960MC target monitor EPROMS. The SDM-960MC and the Ada-960MC debugger are preconfigured to support the EXV-960MC execution environment. Designers can select the software debugger best suited to their development needs. The Ada-960MC debugger is a source-level symbolic debugger which provides a productive debugging environment for Ada applications. The SDM-960MC debug monitor offers a complete debugging facility for applications written in C, assembler or Ada.

\*VAX/VMS is a trademark of Digital Equipment Corp.

## SDM-960MC RETARGETABLE SYSTEM DEBUG MONITOR

### **FEATURES**

- 25 MHz 80960MC processor
- 256 Kbytes of (0,0,0,0) programmable wait-state SRAM
- 4 Mbytes dual-ported (3,1,1,1) wait-state DRAM
- iSBX™ interface
- Two serial ports, one bi-directional parallel port
- 8254 programmable interval timer
- 8259A programmable interrupt controller

### **ELECTRICAL CHARACTERISTICS**

10 A @ +5V

50mA @ +12V

50mA @ -12V

### **ENVIRONMENTAL CHARACTERISTICS**

Operating temperature: 0° to +60°C (32° to 140°F), 300 LFM

Operating Humidity: 10% to 90% non-condensing

### **SOFTWARE DEBUGGING SUPPORT**

The SDM-960MC is a VAX/VMS\*-hosted system debug monitor that provides a complete, flexible environment to execute and debug 80960MC-based applications. Users can tailor the execution environment as software development evolves. Initially, the application may require the full support of the system debug monitor to establish a run-time environment. As the application evolves, the SDM-960MC allows the application to take more of the responsibility for system functions.

The default execution environment of the SDM-960MC is the EXV-960MC execution vehicle. The VAX-hosted portion of the SDM-960MC debug monitor provides complete on-target debugging support through its interface with the target-resident portion of the SDM-960MC. To facilitate debugging on a user's custom target system, the SDM-960MC includes source and object files necessary to reconfigure the target monitor. SDM-960MC and other 80960MC development tools allow the developers to take full advantage of the 80960MC processor.

### **FEATURES**

- assemble and disassemble 80960MC instructions
- single step program execution
- access to memory and processor resources
- support 64 execution breakpoints
- issue Interagent Communications (IACs)
- powerful execution trace
- serial download

### **HARDWARE REQUIREMENTS**

- a serial interface
- 25 Kbytes of EPROM
- contiguous 50 Kbytes of RAM

### **WORLDWIDE SERVICE AND SUPPORT**

Intel augments its 80960 architecture family development tools with a full array of seminars, classes, and workshops; on-site consulting services and telephone support are available at all stages of development.

### **ORDERING INFORMATION**

#### **Product Code Description**

EXV960MC	80960MC execution vehicle (board and target EPROM)
SDM960MC	VAX, MicroVAX/VMS hosted System Debug Monitor, retargetable source is included

